



PATENT
29925/39869

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Hak-Yun Kim

Serial No.: 10/737,052

Filed: December 16, 2003

For: DRAM Cell Having MOS Capacitor and Method for Manufacturing the Same

Group Art Unit: 2818

Examiner: To be assigned

I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, postage prepaid, on **May 12, 2004**, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450

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SUBMISSION OF CORRECTED DECLARATION AND REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Commissioner:

Enclosed herewith is a corrected declaration for the above-referenced patent application. The original declaration filed with this application incorrectly named the priority document. Specifically, the priority document stated on the previously filed declaration is No. 2002-86019. The correct priority document is No. 2002-80298. Accordingly, a corrected declaration is filed herewith.

Applicant requests that a corrected filing receipt be provided naming only the single and correct priority document which is 2002-80298.

Applicant apologizes for any inconvenience. If you have any questions, please do not hesitate to contact the undersigned at the number listed below.

Respectfully submitted,

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May 12, 2004

By:

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